

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

BUTTS, Michael

Serial No.: 10/669,095

Filed: September 23, 2003

**For: LOGIC MULTIPROCESSOR FOR
FPGA IMPLEMENTATION**

Group Art Unit: 2825

Examiner: Yelena Rossoshek

AMENDMENT AND RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed April 19, 2006, please enter the following amendments.